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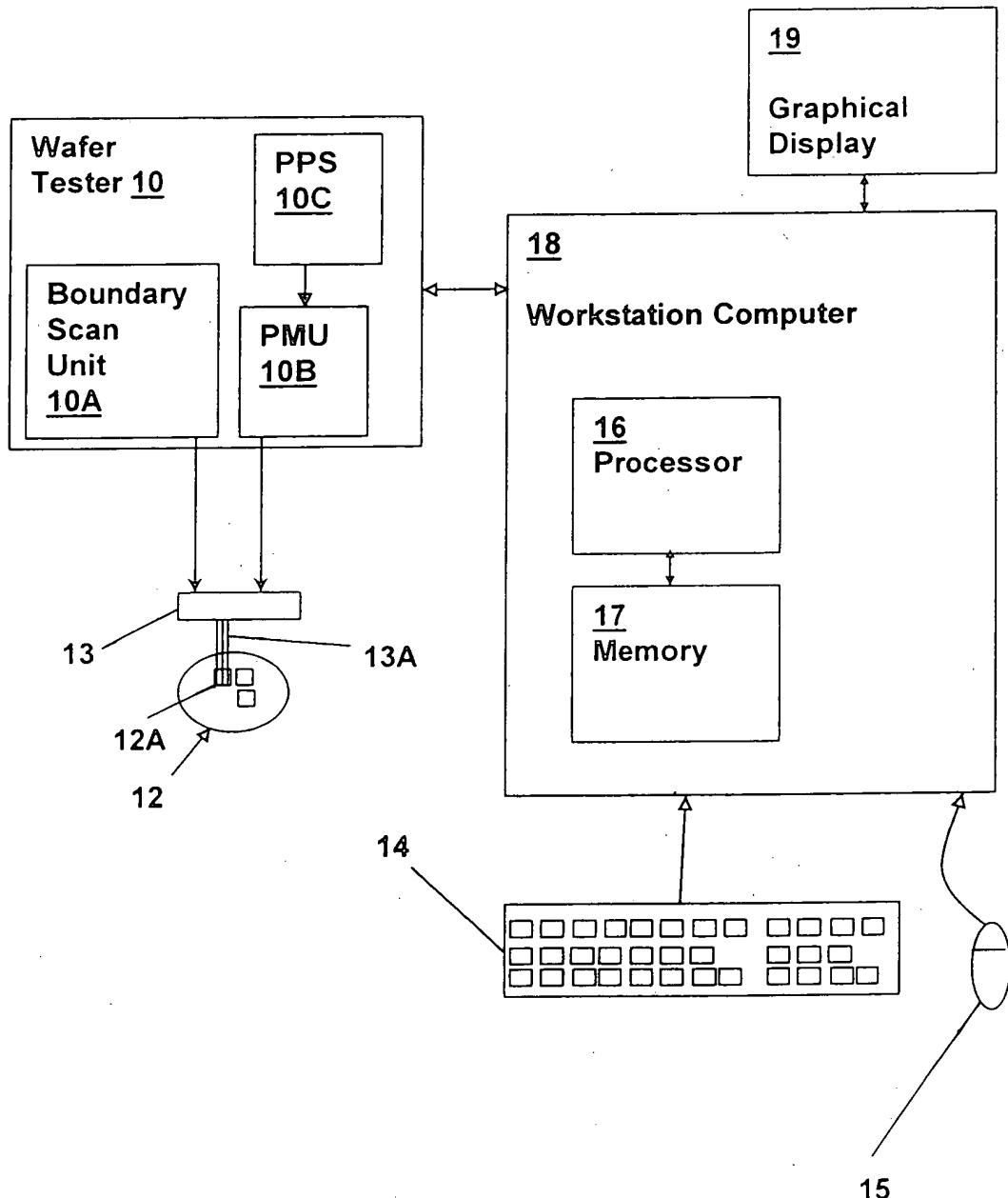


Fig. 1

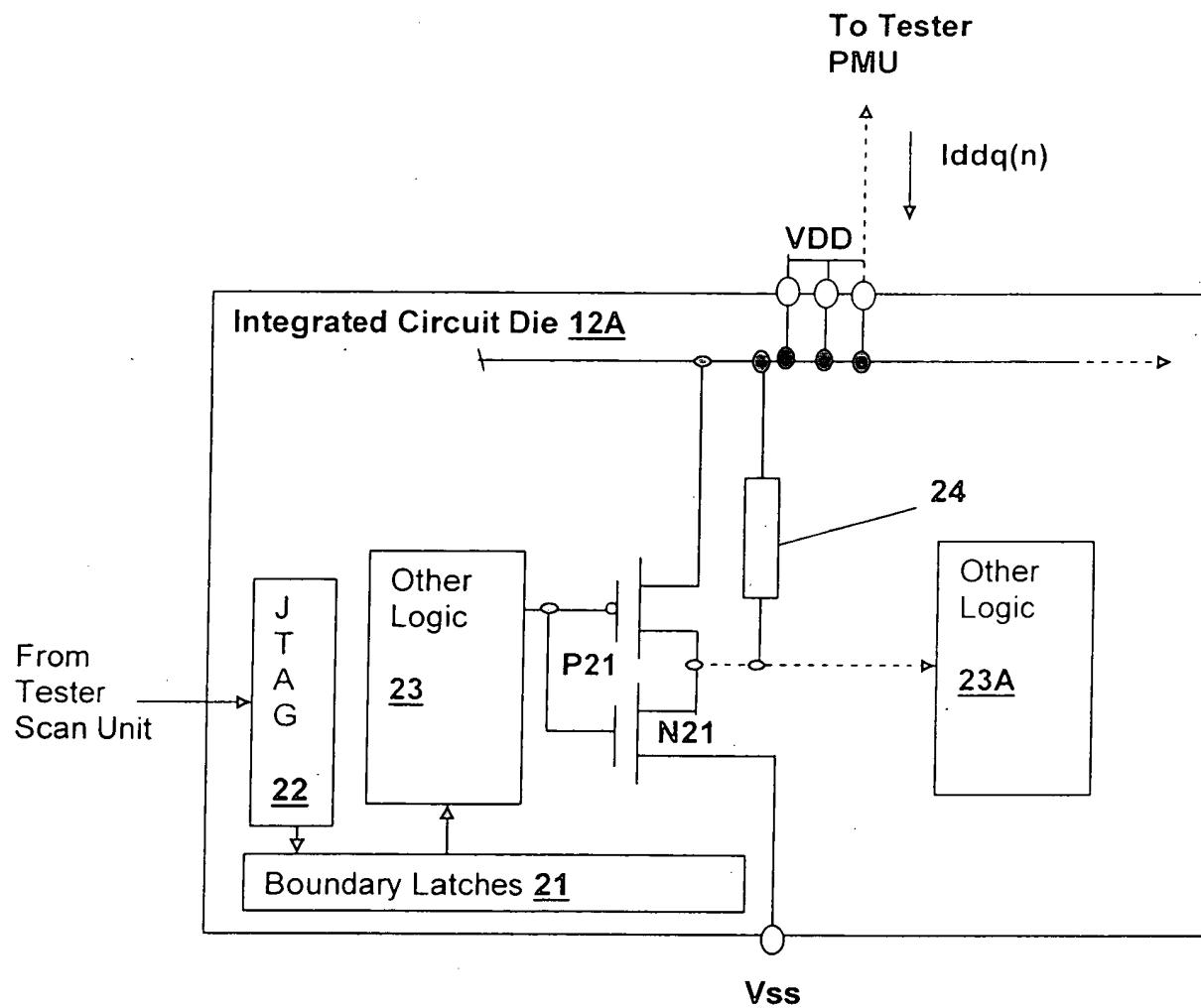


Fig. 2

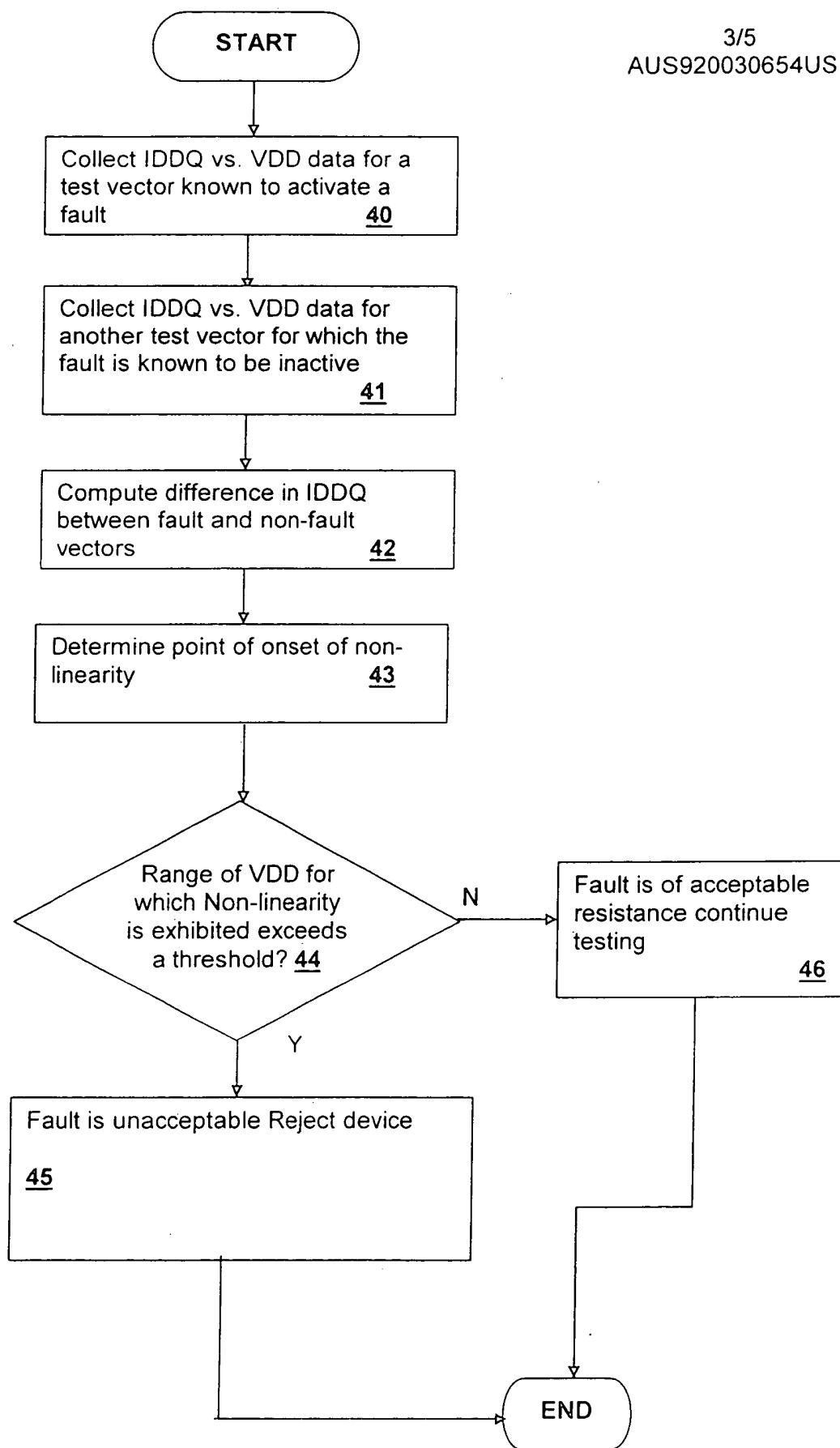


Fig. 3

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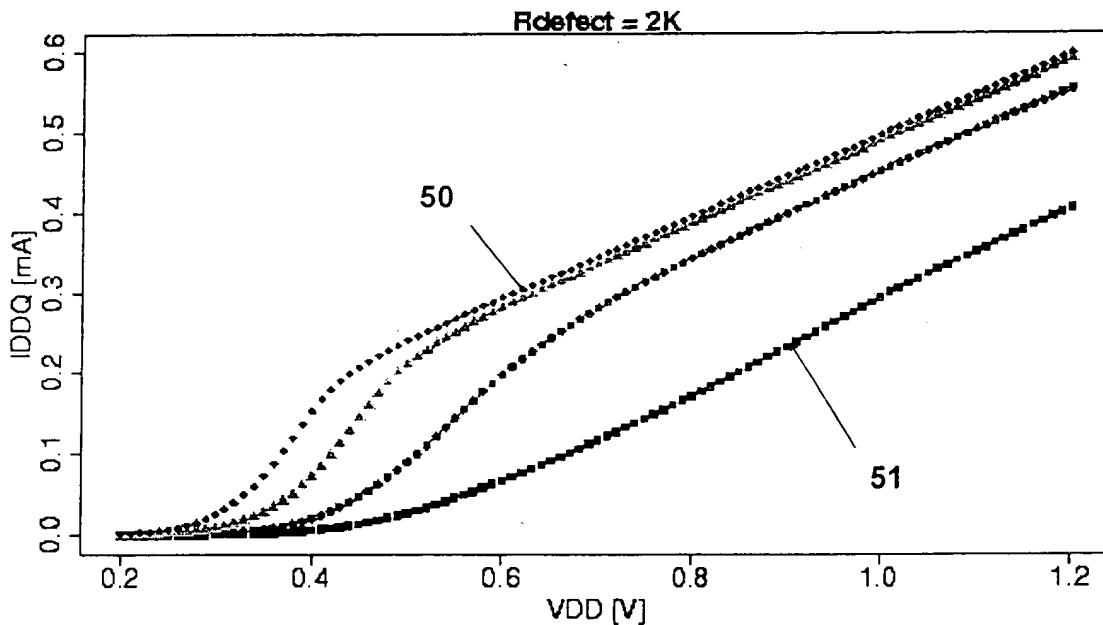


Fig. 4A

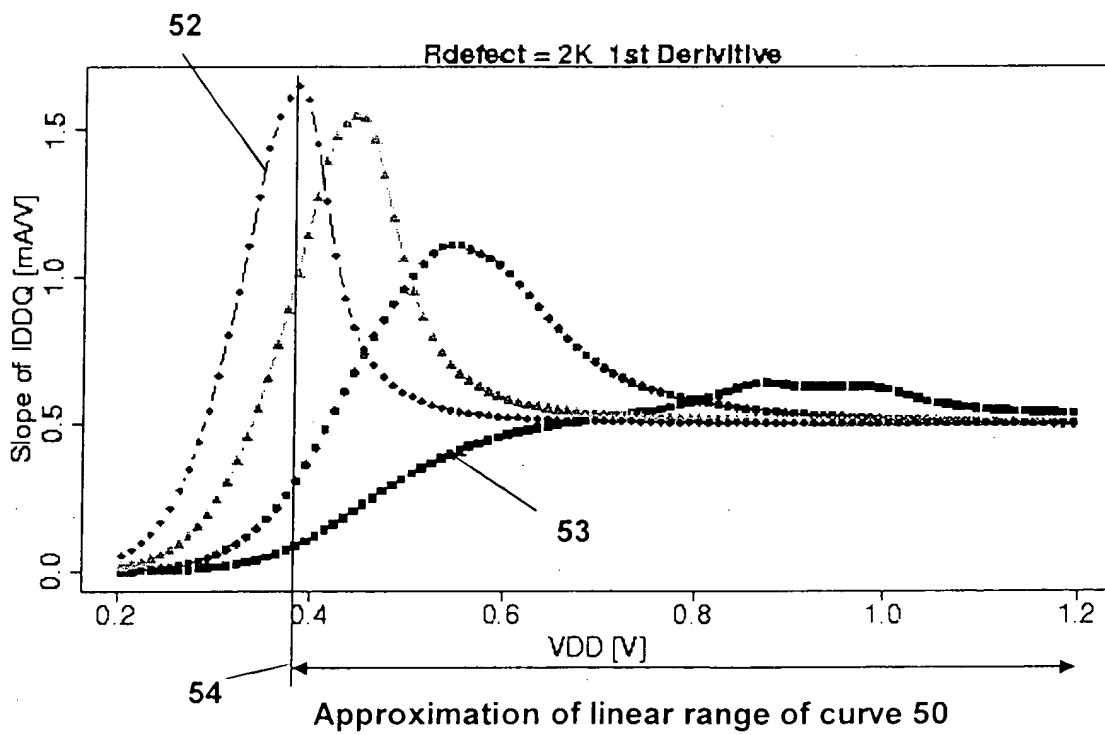


Fig. 4B

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Excess delay versus nonlinearity onset voltage

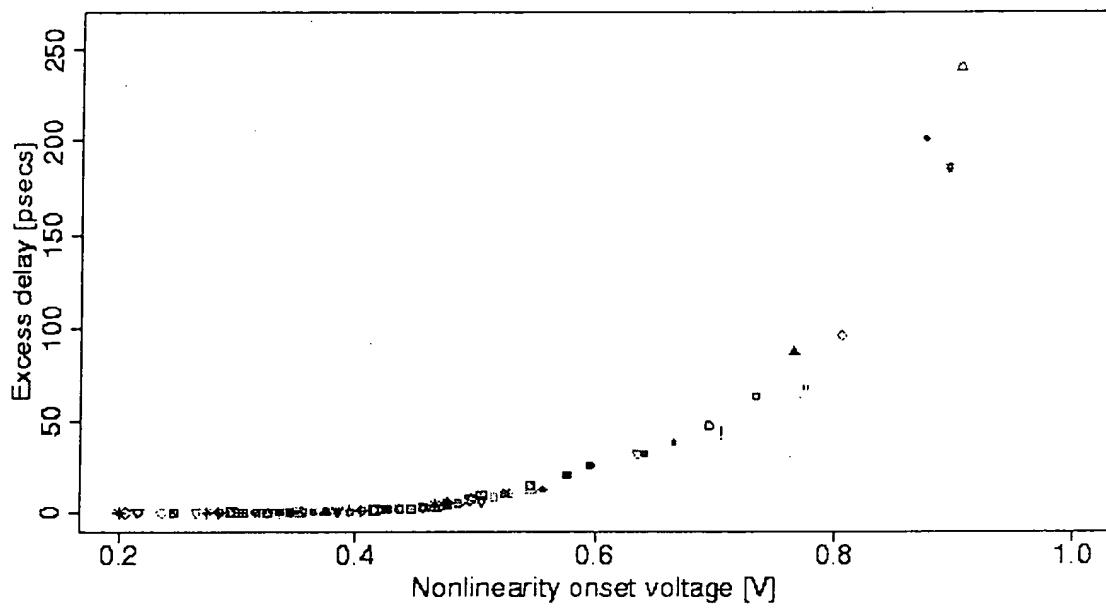


Fig. 4C